



PATENT NUMBER

P

U.S. UTILITY Patent Application

JCS
897
O.P.E.
SCANNED

PATENT DATE

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/725166	F	257	433	2012	LUAN THAI

APPLICANTS

Avner Badéhi

7827

Fig. 1A

TITLE

Methods for producing packaged integrated circuit devices & packaged integrated circuit devices produced thereby

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS-REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER LINE)		
433	133	257	435	431	422
		257	433	613	

(Continued on back of sheet if necessary)

TERMINAL EXAMINER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
NOTICE OF ALLOWANCE MAILED					
(Assistant Examiner) _____ (Date) _____					
(Primary Examiner) _____ (Date) _____					
(Legal Instruments Examiner) _____ (Date) _____					
ISSUE FEE					
Amount Due		Date Paid			
ISSUE BATCH NUMBER					

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Formal Drawn
10/1/95

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